

PAT-NO: JP02000312431A  
DOCUMENT-IDENTIFIER: JP 2000312431 A  
TITLE: DIGITAL PROTECTION CONTROLLER  
PUBN-DATE: November 7, 2000

INVENTOR-INFORMATION:

NAME	COUNTRY
KIDO, MITSUYASU	N/A
CHIBA, TOMIO	N/A
SAITO, SHINJI	N/A
KOBAYASHI, TAKASHI	N/A

ASSIGNEE-INFORMATION:

NAME	COUNTRY
HITACHI LTD	N/A

APPL-NO: JP11119330  
APPL-DATE: April 27, 1999

INT-CL (IPC): H02H003/02

ABSTRACT:

PROBLEM TO BE SOLVED: To automatically correct a digital signal used for protection arithmetic processing according to a detected value by automatically detecting an offset generated in an analog system.

SOLUTION: An analog signal from a power system is taken into an analog filter 1b, and the analog signal is filtered by a multiplexer, while being sampled by an A/D converter 1c and being converted into a digital signal. Digital-filter arithmetic processing is executed to the

digital signal in a  
digital filter 1e, an offset is extracted, and the offset  
is stored in a memory  
1g as an offset correction value. The digital signal by an  
output from the A/D  
converter 1c is subtracted and corrected successively at  
the offset correction  
value from the memory 1g in a subtracter 1h, and protection  
arithmetic  
processing for controlling a relay is conducted in  
accordance with the  
corrected digital signal.

COPYRIGHT: (C)2000,JPO